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INFORMATION DISCLOSURE CITATION IN AN APPLICATION	ATTY. DOCKET NO. **60188-028**	SERIAL NO. **09/779,751**
	APPLICANT **Toshio YAMADA**	
(PTO-1449)	FILING DATE **February 09, 2001**	GROUP **2154**
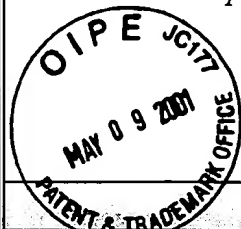
OIPE JC177 MAY 0 9 2001 PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
LV	5,448,749	09/05/95	KYUMA, ET AL.			
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RECEIVED MAY 1 1 2001 Technology Center 2100

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes	No
LV	08/286999	11/01/96	JAPAN (ABSTRACT ONLY)				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
Lhenno	8/8/03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION	ATTY. DOCKET NO. 60188-028	SERIAL NO. (Divisional of Serial No. 09/102,166)
	APPLICANT Toshio YAMADA	
(PTO-1449)	FILING DATE February 09, 2001	GROUP

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
L✓	5,860,142	1/12/99	Cepulis			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes	No
L✓	05150943	6/18/93	Japan				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

L✓	"Processing in Memory: The Terasys Massively Parallel PIM Array," by Gokhale et al., IEEE, pp. 23-31.
L✓	Mano, Computer System Architecture, Prentice-Hall, Inc., pg. 39.

EXAMINER	DATE CONSIDERED
	1/8/03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.